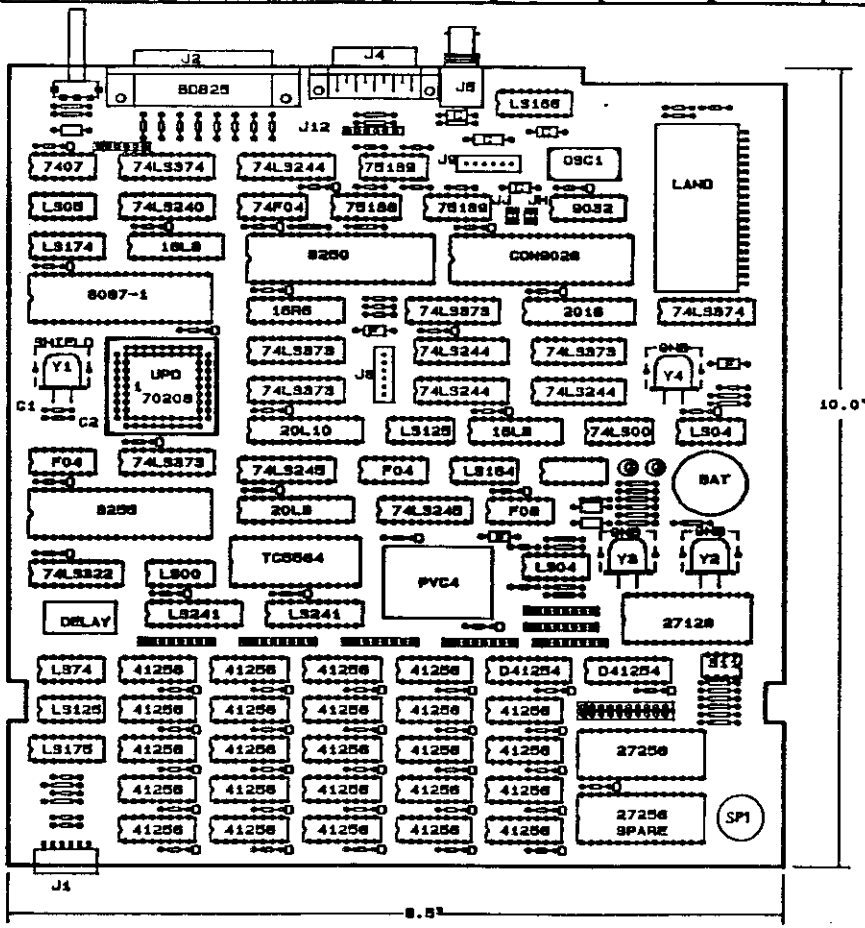
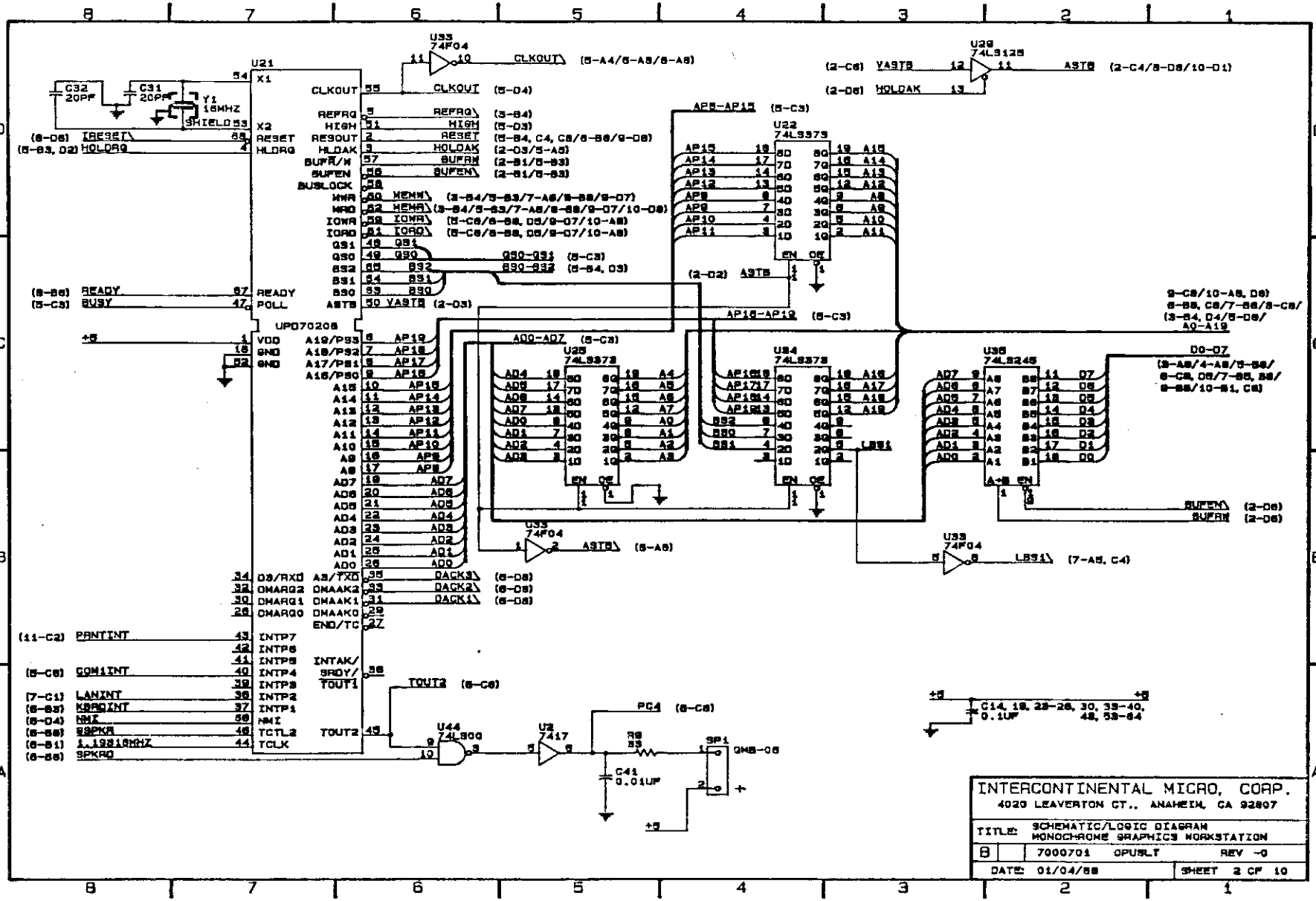
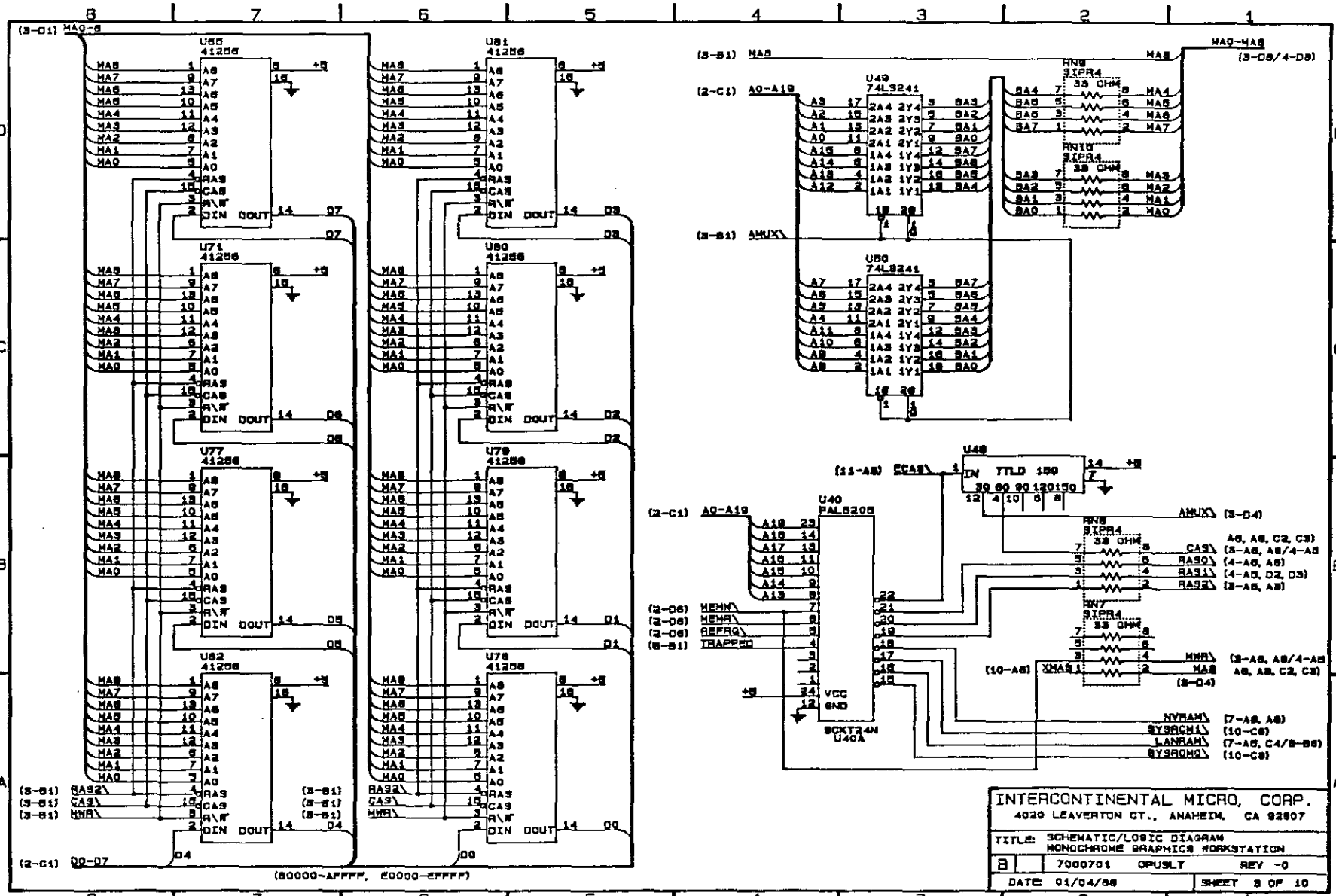
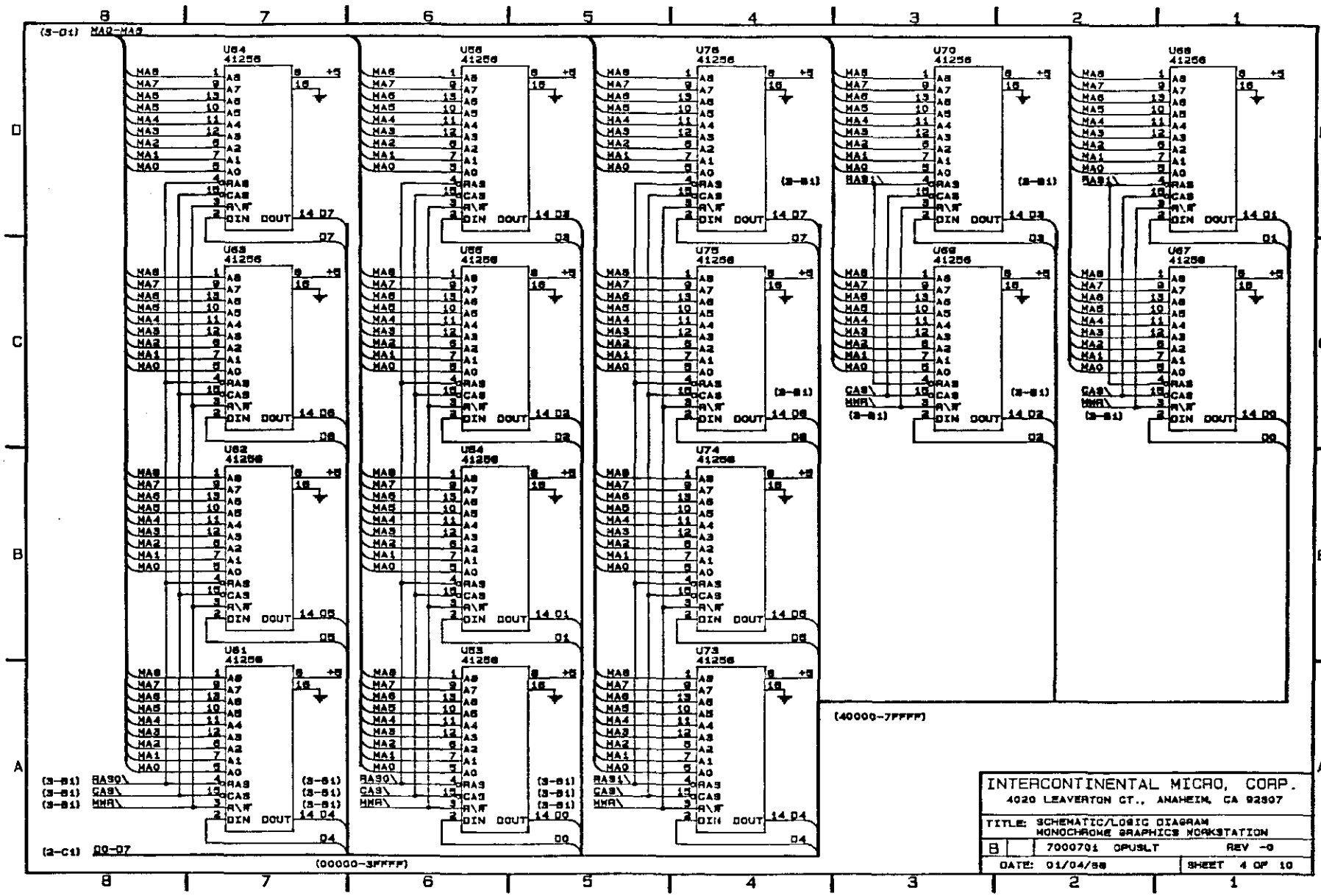


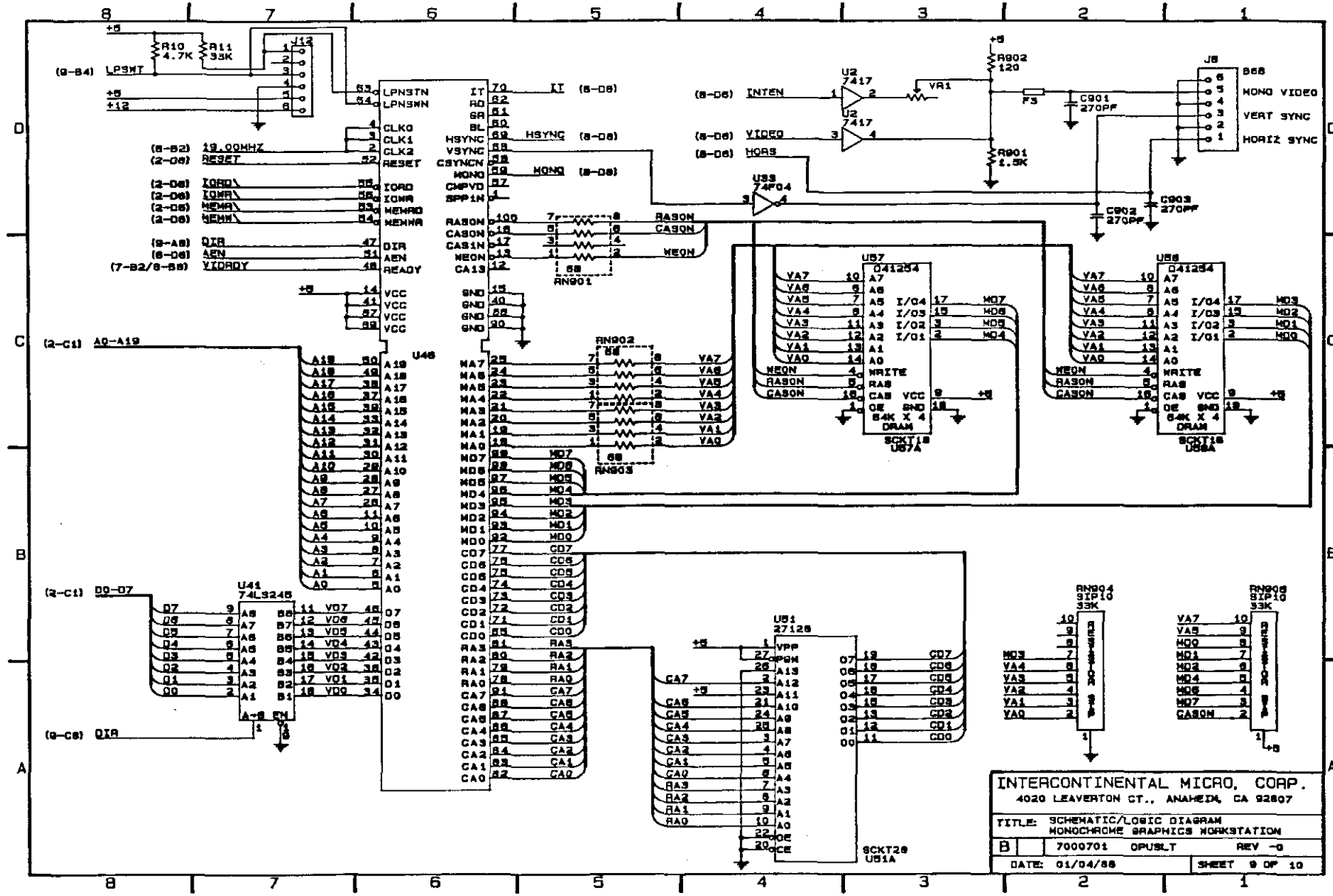
REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVAL

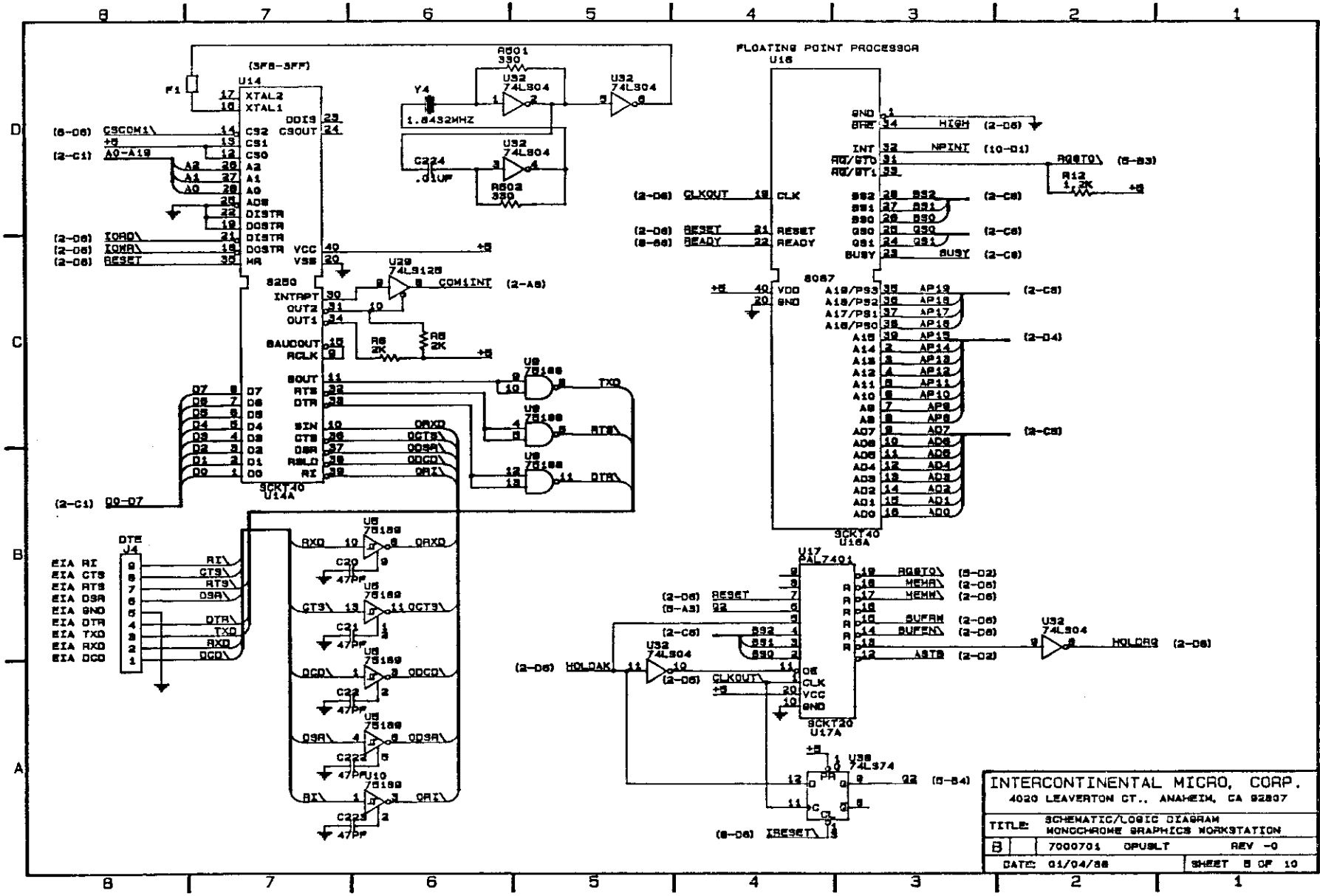
INTERCONTINENTAL MICRO, CORP. 4020 LEAVERTON CT., ANAHEIM, CA 92807			
TITLE:	PCB LAYOUT (PROPOSED) MONOCHROME GRAPHICS WORKSTATION		
B		REV -	
DATE: 12/12/87		SHEET 1 OF 10	

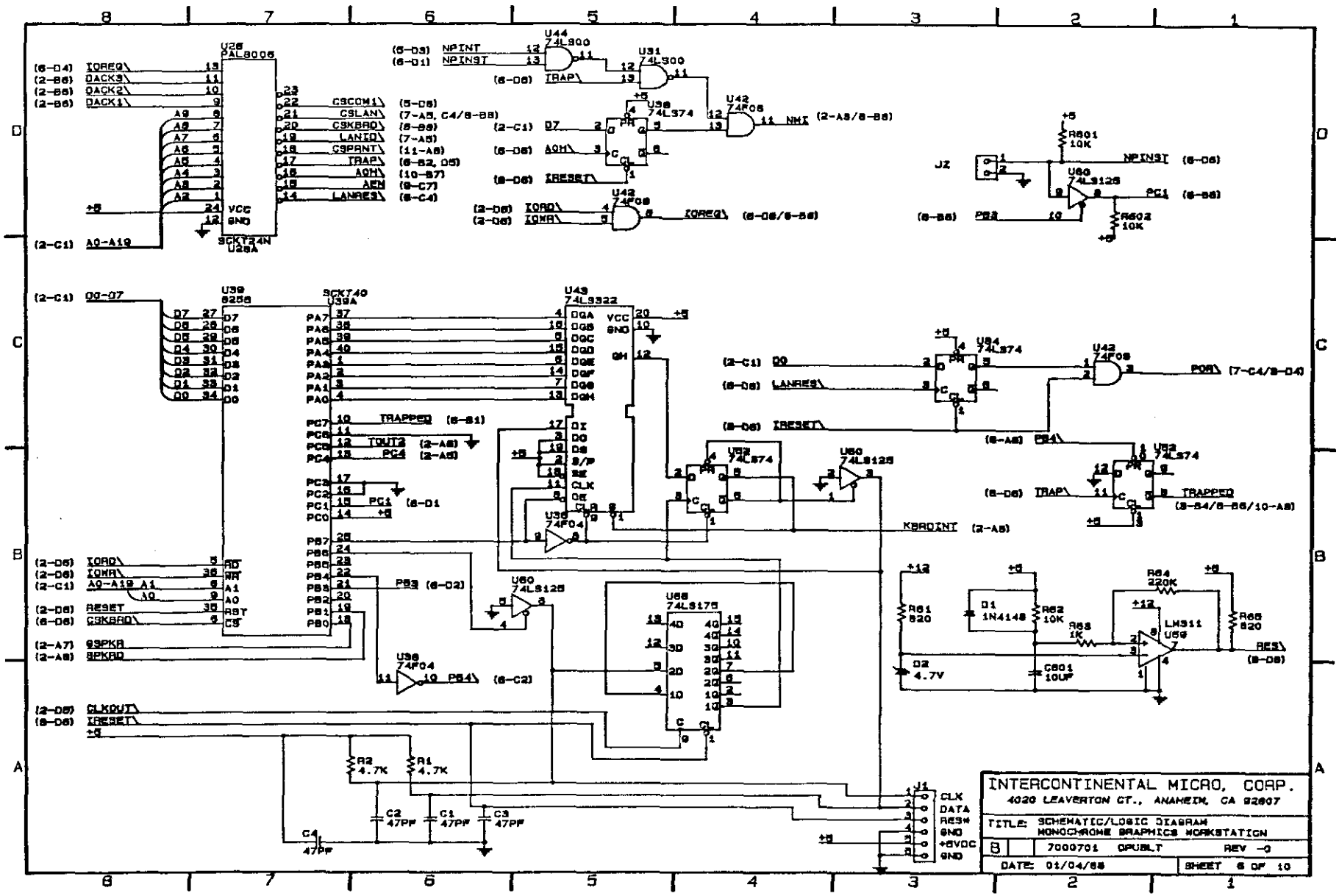

U21
U33 74F04
U29 74LS125
U22 74LS373
U24 74LS373
U25 74LS373
U36 74LS245
UPD70208
U44 74LS00
U2 7417
C32 20PF
C31 20PF
Y1 16MHZ
SHIELD
C41 0.01UF
SP1
QMB-06
R9 83
INTERCONTINENTAL MICRO, CORP.
4020 LEAVERTON CT., ANAHEIM, CA 92807
TITLE: SCHEMATIC/LOGIC DIAGRAM MONOCHROME GRAPHICS WORKSTATION
8 7000701 GPUSLT REV -0
DATE: 01/04/88 SHEET 2 OF 10

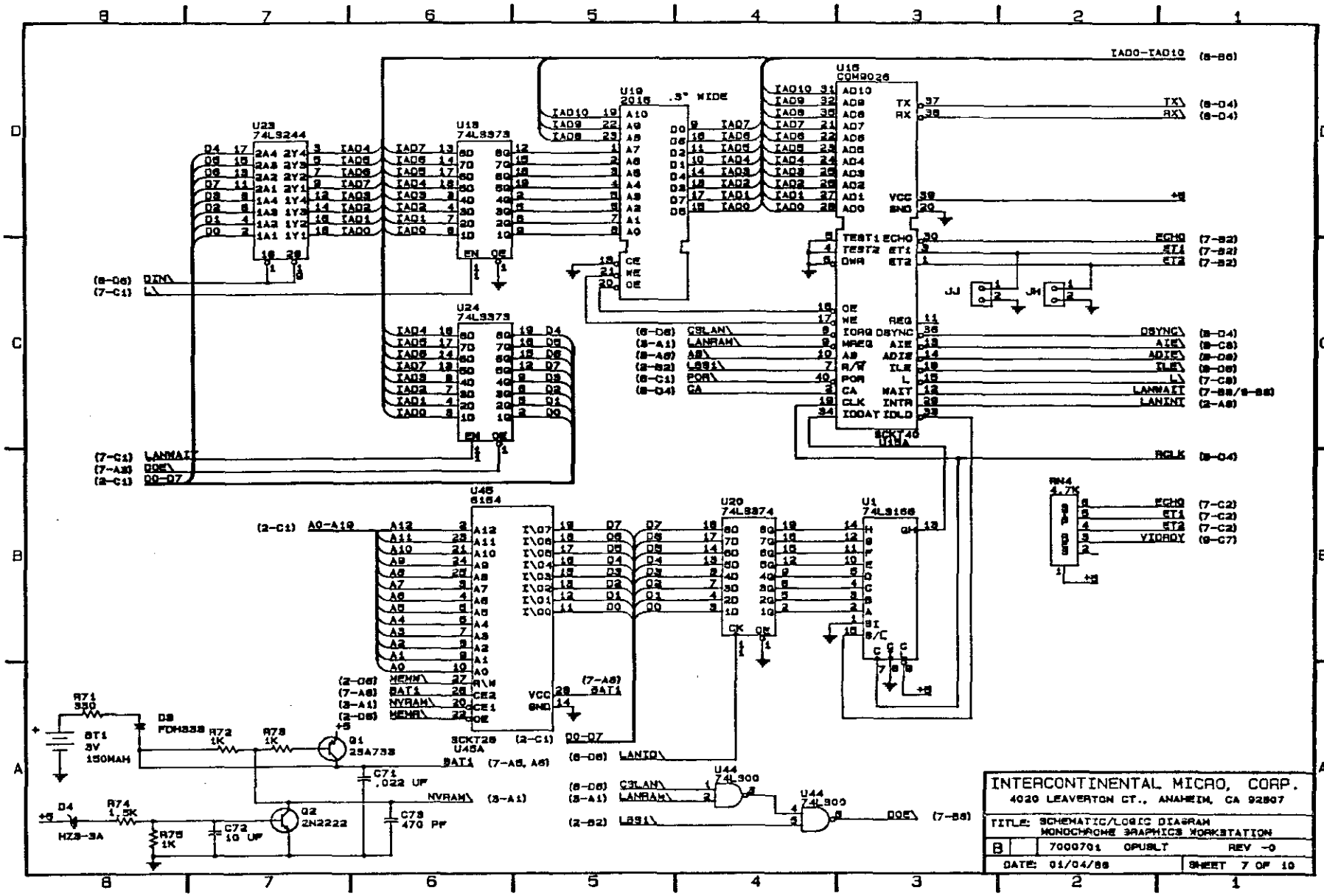

(3-D1) MA0-8
U65 41256
U71 41256
U77 41256
U82 41256
U81 41256
U80 41256
U79 41256
U78 41256
(3-B1) RAS2\
(3-B1) CAS\
(3-B1) MWR\
(2-C1) D0-D7
(80000-AFFFF, E0000-EFFFF)
(3-B1) MA8
(2-C1) A0-A19
U49 74LS241
U50 74LS241
MA0-MA8
(3-D8/4-D8)
RN8 9IPR4 33 OHM
RN10 9IPR4 33 OHM
(3-B1) AMUX\
U48 TTL2 150
(11-A8) ECAS\
U40 PAL8206
(2-C6) MEMW\
(2-C6) MEMR\
(2-C6) REFRQ\
(6-B1) TRAPPED
SCKT24N U40A
AMUX\ (3-D4)
CAS\ (3-A6, A8/4-A8)
RAS0\ (4-A6, A8)
RAS1\ (4-A5, D2, D3)
RAS2\ (2-A6, A8)
MWR\ (3-A6, A8/4-A8)
MA8 A6, A8, C2, C3)
(3-D4)
(10-A6) XMA8
NVRAM\ (7-A8, A6)
SYSROM1\ (10-C6)
LANRAM\ (7-A5, C4/9-B6)
SYSROM0\ (10-C8)
INTERCONTINENTAL MICRO, CORP.
4020 LEAVERTON CT., ANAHEIM, CA 92807
TITLE: SCHEMATIC/LOGIC DIAGRAM
MONOCHROME GRAPHICS WORKSTATION
B 7000701 OPUSLT REV -0
DATE: 01/04/88
SHEET 3 OF 10

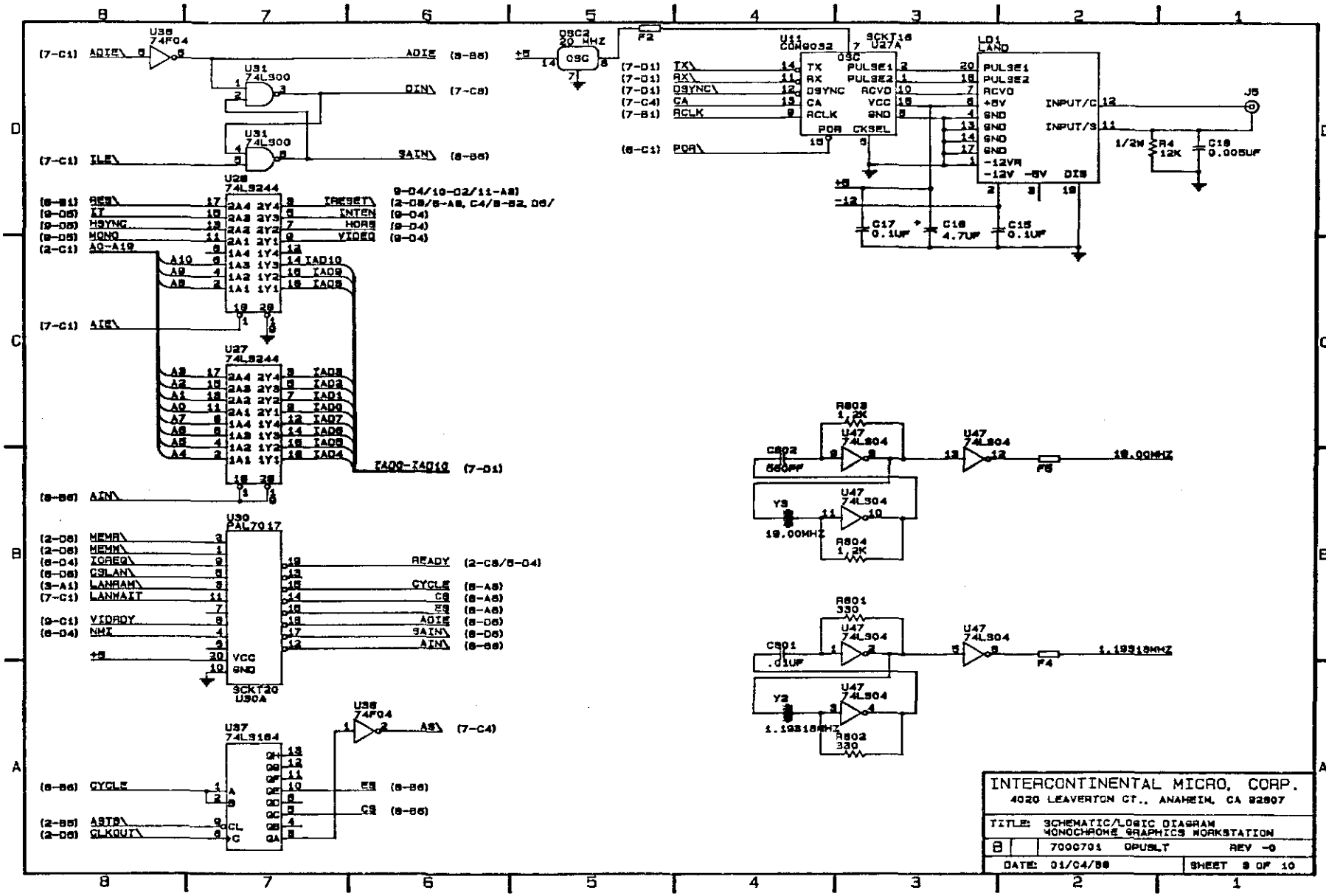

(3-01) MA0-MA8
U64 41256
U63 41256
U62 41256
U61 41256
U66 41256
U65 41256
U64 41256
U63 41256
U76 41256
U75 41256
U74 41256
U73 41256
U70 41256
U69 41256
U68 41256
U67 41256
MA0 MA1 MA2 MA3 MA4 MA5 MA6 MA7 MA8
RAS CAS R\W DIN DOUT
(3-01) RAS0\
(3-01) CAS\
(3-01) MWR\
(3-01) RAS1\
(2-C1) D0-D7
(00000-3FFFF)
(40000-7FFFF)
INTERCONTINENTAL MICRO, CORP.
4020 LEAVERTON CT., ANAHEIM, CA 92807
TITLE: SCHEMATIC/LOGIC DIAGRAM
MONOCHROME GRAPHICS WORKSTATION
B 7000701 GPUSLT REV -0
DATE: 01/04/88
SHEET 4 OF 10

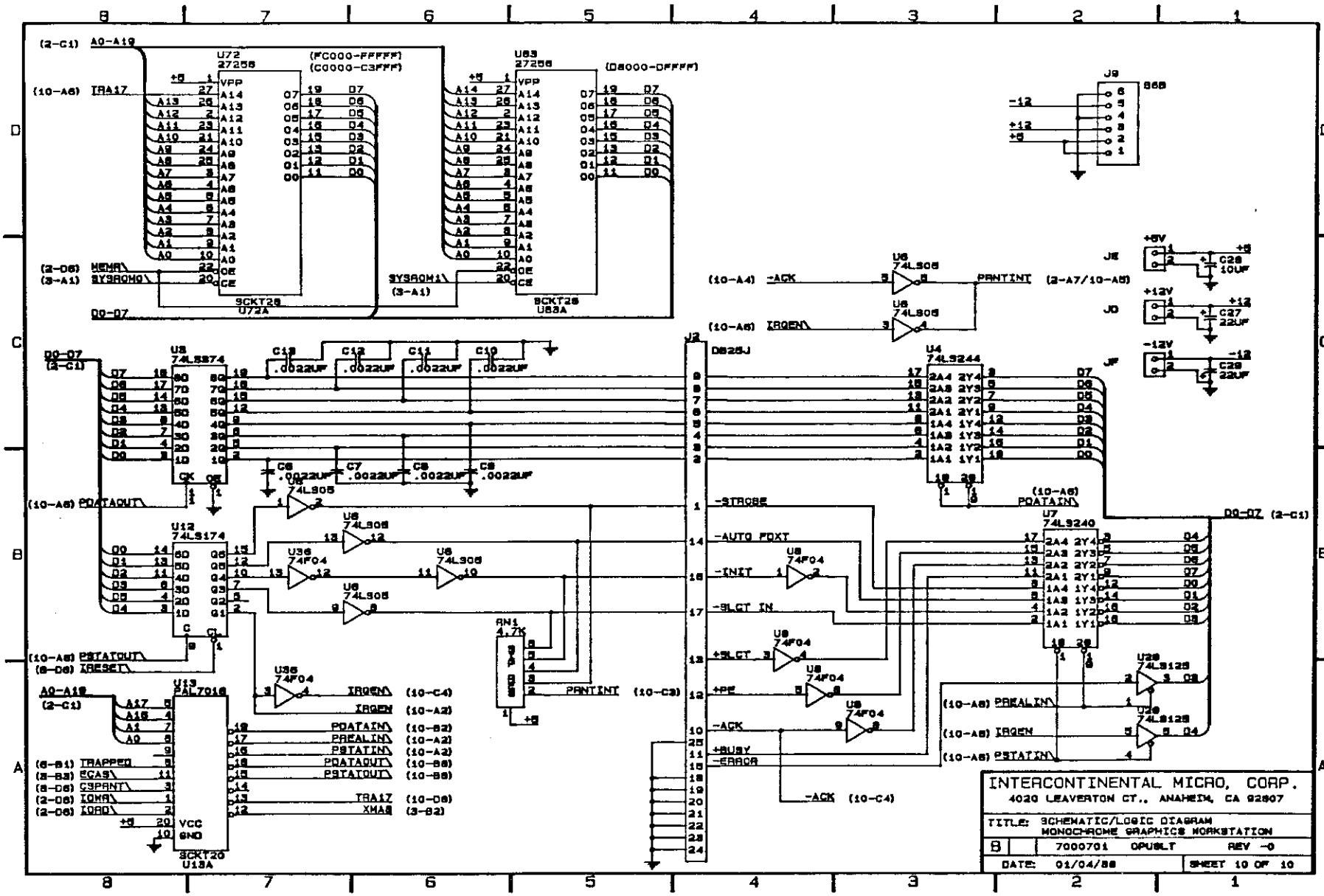

INTERCONTINENTAL MICRO, CORP.
4020 LEAVERTON CT., ANAHEIM, CA 92807
TITLE: SCHEMATIC/LOGIC DIAGRAM
MONOCHROME GRAPHICS WORKSTATION
B
7000701
OPUSLT
REV -0
DATE: 01/04/88
SHEET 9 OF 10
LPSMT
RESET
IORD\
IOWR\
MEMRD\
MEMWR\
DIR
AEN
VIDRDY
INTEN
VIDEO
HORS
HSYNC
MONO
RASON
CASON
MEON
MONO VIDEO
VERT SYNC
HORIZ SYNC
A0-A19
D0-D7
U46
U41
74LS245
U51
27128
U57
U58
41256
64K X 4 DRAM
SCKT18
SCKT28


FLOATING POINT PROCESSOR
U16
(3F8-3FF)
U14
XTAL2
XTAL1
CS2
CS1
CS0
A2
A1
A0
ADS
DISTR
DOSTR
MR
VCC
VSS
8250
INTRPT
OUT2
OUT1
BAUDOUT
RCLK
SOUT
RTS
DTR
SIN
CTS
DSR
RSLD
RI
D7
D6
D5
D4
D3
D2
D1
D0
SCKT40
U14A
Y4
1.8432MHZ
C224
.01UF
R501
330
R502
330
U32
74LS04
U29
74LS125
COMIINT
R6
2K
R5
2K
U9
75188
TXD
RTS
DTR
U6
75189
RXD
GRXD
GCTS
GDSR
GDCD
GRI
C20
C21
C22
C222
C223
47PF
U10
DTE
J4
EIA RI
EIA CTS
EIA RTS
EIA DSR
EIA GND
EIA DTR
EIA TXD
EIA RXD
EIA DCD
U16
GND
SRE
HIGH
INT
NPINT
RQ/GT0
RQ/GT1
RQGT0\
R12
1.2K
CLK
RESET
READY
S2
S1
S0
QS0
QS1
BUSY
8087
VDD
VSS
A19/PS3
A18/PS2
A17/PS1
A16/PS0
A15
A14
A13
A12
A11
A10
A9
A8
AD7
AD6
AD5
AD4
AD3
AD2
AD1
AD0
AP19
AP18
AP17
AP16
AP15
AP14
AP13
AP12
AP11
AP10
AP9
AP8
SCKT40
U16A
U17
74LS491
MEMR
MEMW
BUFRM
BUFEN
ASTB
SCKT20
U17A
HOLDAK
HOLDRQ
U38
74LS374
IRESET\
INTERCONTINENTAL MICRO, CORP.
4020 LEAVERTON CT., ANAHEIM, CA 92807
TITLE: SCHEMATIC/LOGIC DIAGRAM
MONOCHROME GRAPHICS WORKSTATION
B
7000701 OPUSLT
REV -0
DATE: 01/04/88
SHEET 5 OF 10


INTERCONTINENTAL MICRO, CORP.
4020 LEAVERTON CT., ANAHEIM, CA 92807
TITLE: SCHEMATIC/LOGIC DIAGRAM
MONOCHROME GRAPHICS WORKSTATION
B 7000701 OPUBLT REV -0
DATE: 01/04/85 SHEET 6 OF 10


U23 74LS244
U18 74LS373
U19 2016 .3" WIDE
U15 COM9026
U24 74LS373
U45 6164
U20 74LS374
U1 74LS166
U44 74LS00
U45A
SCKT28
SCKT40 U15A
RN4 4.7K
BT1 3V 150MAH
D3 FDH333
Q1 2SA733
Q2 2N2222
D4 HZ3-3A
C71 .022 UF
C72 10 UF
C73 470 PF
R71 330
R72 1K
R73 1K
R74 1.5K
R75 1K
IAD0-IAD10 (8-B6)
TX\ (8-D4)
RX\ (8-D4)
ECHO (7-B2)
ET1 (7-B2)
ET2 (7-B2)
DSYNC\ (8-D4)
AIE\ (8-C8)
ADIE\ (8-D8)
ILE\ (8-D8)
L\ (7-C8)
LANWAIT (7-B8/8-B8)
LANINT (2-A8)
RCLK (8-D4)
VIDRDY (8-C7)
DIN\ (8-D6)
L\ (7-C1)
LANWAIT (7-C1)
DOE\ (7-A3)
D0-D7 (2-C1)
A0-A19 (2-C1)
MEMW\ (2-D6)
BAT1 (7-A8)
NVRAM\ (2-A1)
MEMR\ (2-D6)
LANIO\ (8-D6)
C3LAN\ (8-D6)
LANRAM\ (8-A1)
L891\ (2-B2)
DOE\ (7-B8)
INTERCONTINENTAL MICRO, CORP.
4020 LEAVERTON CT., ANAHEIM, CA 92807
TITLE: SCHEMATIC/LOGIC DIAGRAM MONOCHROME GRAPHICS WORKSTATION
B 7000701 OPUSLT REV -0
DATE: 01/04/88 SHEET 7 OF 10


U38 74F04
U31 74LS00
U28 74LS244
U27 74LS244
U30 PAL7017
U37 74LS164
OSC2 20 MHZ
U11 COM8032
U27A
LD1 LAND
J5
R803 1.2K
R804 1.2K
R801 330
R802 330
C802 .068UF
C801 .01UF
C17 0.1UF
C18 4.7UF
C15 0.1UF
R4 12K
C16 0.005UF
Y3 19.00MHZ
Y2 1.19318MHZ
F2
F5
F4
INTERCONTINENTAL MICRO, CORP.
4020 LEAVERTON CT., ANAHEIM, CA 92807
TITLE: SCHEMATIC/LOGIC DIAGRAM MONOCHROME GRAPHICS WORKSTATION
B 7000701 OPUSLT REV -0
DATE: 01/04/88 SHEET 8 OF 10


INTERCONTINENTAL MICRO, CORP.
4020 LEAVERTON CT., ANAHEIM, CA 92807
TITLE: SCHEMATIC/LOGIC DIAGRAM
MONOCHROME GRAPHICS WORKSTATION
B 7000701 OPUSLT REV -0
DATE: 01/04/88 SHEET 10 OF 10
U72 27256 (FC000-FFFFF) (C0000-C3FFF)
U83 27256 (D8000-DFFFF)
SCKT28 U72A
SCKT28 U83A
U8 74LS374
U12 74LS174
U13 PAL7016
SCKT20 U13A
U2 DB25J
U4 74LS244
U7 74LS240
U6 74LS06
U8 74F04
U28 74LS125
J9 868
J8 C28 10UF
J0 C27 22UF
JF C28 22UF
RN1 4.7K
C13 .0022UF
C12 .0022UF
C11 .0022UF
C10 .0022UF
C6 .0022UF
C7 .0022UF
C8 .0022UF
C9 .0022UF
A0-A19
D0-D7
MEMR\
SYSROM0\
SYSROM1\
TRA17
TRAPPED
ECAS\
C3PANT\
IOWR\
IORD\
XMAS
PRINTINT
IRQEN
PDATAIN
PREALIN
PSTATIN
PDATAOUT
PSTATOUT
IRESET
-ACK
-STROBE
-AUTO FDXT
-INIT
-SLCT IN
+SLCT
+PE
+BUSY
-ERROR
D0-D7 (2-C1)